CLAIM AMENDMENTS

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) An apparatus comprising:

a housing;

a mainboard including memory and a first processor mounted within the housing;

a first network interface disposed within the housing and operatively coupled to the first processor, having a first network port and a first address;

at least one expansion slot for receiving a peripheral device, operatively coupled to the mainboard; and

a network communications link connecting the first network interface to said at least one expansion slot substantially disposed within the housing,

wherein the first processor is enabled to communicate with a peripheral device adapted to be coupled to one of said at least one expansion slot and having a built-in network interface by transmitting data via the first network interface and the built-in network interface over the network communications link using packetized messages based on a network transmission protocol.

2. (Previously Presented) The apparatus of claim 1, further comprising a second network interface disposed on the mainboard in proximity to one of said at least one expansion slot having a second address and a second network port to enable communication between the first processor and a peripheral device that does not include

a built-in network interface when the peripheral device is mounted in the one of said at least one expansion slots. .

3. (Original) The apparatus of claim 1, wherein the network communications link comprises a network bus embedded in the mainboard.

4. (Original) The apparatus of claim 1, wherein the first network interface and the communications link comprise an Ethernet subnet.

5. (Previously Presented) The apparatus of claim 1, further comprising:

a second processor coupled to the mainboard; and

a second network interface operatively coupled to the second processor and the network communications link to enable communication between the second processor and a peripheral device having a built-in network interface when the peripheral device is mounted in one of said at least one expansion slots.

6. (Currently Amended) A system comprising:

a computing machine including:

a housing; and

a mainboard to which memory and a first processor are connected, said mainboard providing a first network interface operatively coupled to the first processor having a first network port and a first address;

a first peripheral device disposed within the housing;

a second network interface operatively coupled to the mainboard, providing a second network port and a second network address, linked in communication with the first peripheral device; and

a communications link between the first and second network interfaces substantially disposed within the housing, the communication link using packetized messages based on a network transmission protocol.

7. (Original) The system of claim 6, wherein the communications link and the first and second network interfaces comprise an Ethernet subnet.

8. (Original) The system of claim 6, wherein the communication link comprises a network signal bus built into the mainboard.

9. (Original) The system of claim 6, wherein the communications link comprises a token ring.

10. (Original) The system of claim 6, wherein the second network interface is built into the first peripheral device;

11. (Original) The system of claim 6, wherein the second network interface is built into the mainboard.

12. (Original) The system of claim 6, wherein the peripheral device comprises one of a video subsystem, a memory subsystem, a disk controller and a modem.

13. (Original) The system of claim 6, wherein the mainboard further includes a second processor connected to a third network interface having a third network address and network port connected to the communications link.

14. (Currently Amended) A method for enabling communication between a peripheral device disposed within a computing machine having a processor and an application running on the processor, comprising:

providing a network interface for each of the processor and the peripheral device;

providing a communication link between the network interfaces of the processor and the peripheral device;

creating a network socket for each of the processor and the peripheral device;

establishing a connection between the processor and the peripheral device;

generating messages with the application;

transferring the messages between the processor and the peripheral device using packetized messages based on a network transmission protocol.

15. (Previously Presented) The method of claim 14, wherein the network transmission protocol comprises the TCP/IP protocol.

16. (Previously Presented) The method of claim 14, further comprising applying security measures to determine if an application may connect to a particular peripheral device.

17. (Previously Presented) The method of claim 14, wherein the network transmission protocol comprises the UDP protocol.

18. (Previously Presented) The method of claim 14, wherein the communications link and the network interfaces comprise an internal Ethernet network.

19. (Previously Presented) The method of claim 14, wherein the communications link and the network interfaces comprise an internal token ring network.

20. (Previously Presented) The system of claim 6, further comprising:

a storage device on which software is stored, the software comprising machine instructions that are executable by the first processor that includes a socket application interface (API) that binds the address of the first peripheral device to the second network port and a network interface abstraction layer that provides an abstracted interface that enables an application to communicate with the first peripheral device using a networking protocol.